

PRESS RELEASE

PCIe, RapidIO, and Ethernet for High-Speed Interconnects to be Focus of Linley Tech Seminar

MOUNTAIN VIEW, CA—April 26, 2007—On May 9, The Linley Group will present a Linley Tech seminar on "High-Speed Interconnects" featuring top technologists and industry leaders discussing the newest products and technologies for PCIe, RapidIO, Ethernet and other leading interconnects.

The morning session focuses on the latest developments in system-level interconnects, featuring talks from leading silicon technology suppliers on signal integrity, system design, and switching. It also addresses the design challenges of using high-speed interconnects and how different interconnects can be combined in real systems.

If your interest lies in the recent advances at the physical layer for Ethernet at 10Gbps and beyond, the afternoon session examines solutions using 10GBase-T and LRM—new standards developed to meet evolving system requirements for optical and electrical. We'll examine both technologies and provide you with critical information you need in evaluating the appropriate technologies and products for your designs.

The day will wrap up with a lively closing panel, moderated by Jag Bolaria, focusing on the relative strengths and merits of leading interconnects. The audience will have the opportunity to grill a panel of experts on Ethernet, RapidIO, and PCIe.

You won't want to miss this outstanding lineup of presenters, including:

-- George A. Zimmerman, CTO, Solarflare Communications, will present "Delivering 10GBase-T Silicon"

-- Alex Dickinson, President and CEO, Luxtera, speaking on "Silicon Photonics - The Next Wave in Low-Cost, High-Performance Optical Interconnect"

-- Sanjay Kasturia, CTO, Teranetics, will present "10GBase-T: A Technology for Upgrading LANs"

-- Peter Yan, Systems Engineer, Freescale, speaking on "High-speed Interconnect Support for Quadruple-play Convergence"

-- Jeff Dodson, Principal Architect, PLX Technology, will present "Measuring Performance for PCIe Systems"

-- Brad Booth, Senior Principal Engineer, AMCC, speaking on "10Gbps Ethernet for the Enterprise"



-- Bernhard Friebe, Sr. Product Marketing Engineer, Altera, will present "High-Speed FPGA Interconnect Solutions"

-- Kimkinyona Fox, Product Marketing Manager, Rambus, speaking on "Integration Considerations for High-Performance I/O"

-- Devashish Paul, Product Marketing Manager, Tundra, speaking on "Developing Wireless Infrastructure using ATCA and RapidIO"

-- Ken Curt, Product Manager, Pericom, will present "Extending Reach for PCIe and Modern Interconnects"

Following the closing panel will be a networking reception, complete with exhibits and a raffle, providing the opportunity for attendees to meet with industry leaders, speakers, analysts, designers, and their colleagues.

Admission is free to qualified individuals who register by May 4. The seminar is intended for OEMs, board developers, software developers, press, and the financial community. The seminar will be held at the DoubleTree Hotel in San Jose and is sponsored by Freescale, AMCC, Altera, Rambus, Pericom, Teranetics, Solarflare, Tundra, and The Linley Group.

For further details and registration information, visit The Linley Group web site:
<http://www.linleygroup.com/seminars.html>

About The Linley Group

The Linley Group, the leading provider of independent technology analysis for networking, communications, and consumer electronics semiconductors, covers emerging areas such as high-speed embedded processors, Gigabit and 10G Ethernet, wireless handset processors, and more. The company provides in-depth technology reports and interactive seminars as well as strategic consulting services tailored to the individual client. To get free access to The Linley Group's analysis of recent news and events in these markets, subscribe to our e-mail newsletters. More information about The Linley Group is available at <http://www.linleygroup.com>.

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