

## **PRESS RELEASE**

### **The Linley Group Seminar Targets Advances in CPU Cores and Intellectual Property for Networking and Communications ASICs and SoCs**

MOUNTAIN VIEW, CA—January 12, 2006—The Linley Group kicks off the first event in its Linley Tech 2006 seminar series on January 25 in San Jose with a free one-day seminar on CPU cores and other licensable intellectual property (IP). Leading IP vendors will explain how their technology can be used in networking and communications applications providing a quick time to market for new designs. This event, sponsored by Freescale, SafeNet, Tensilica, and ARC International, brings together technical experts presenting innovative solutions to design challenges.

The Linley Group, the leading supplier of technology analysis for networking silicon, continues its tradition of providing independent coverage of important topics in networking-system design. The seminar provides critical information to help ASIC and system-on-a-chip (SoC) designers choose the right IP and integrate it into their designs.

You'll have the opportunity to hear industry experts Linley Gwennap and Joseph Byrne, as well as architects and technologists from leading vendors as they deliver information-packed technical presentations in two key areas: first, CPU Cores for Communications Applications and second, Networking IP such as Encryption, 10G Ethernet, PCI Express, and High-Speed Serdes. The seminar wraps up with a panel titled "Avoiding the Pitfalls and Pratfalls of Licensed IP." Following the seminar will be a reception providing opportunities for you to network with these industry leaders, analysts, and your colleagues.

The following are program highlights not to be missed:

- Linley Gwennap, principal analyst of The Linley Group, will provide a CPU and IP overview including a discussion of the advantages and disadvantages of licensing IP for ASIC and system-on-a-chip (SoC) designs. ”
- Ashish Dixit, VP Hardware, Tensilica, "Networking Applications for Xtensa Configurable Processors"
- Peter Wells, Senior Architect, ARC International, "Fitting the Processor to the System"
- Mark Naumann, SoC Architect, Freescale, "Using PowerPC Cores in Communications SoCs"
- Egied Bormans, Director Product Management, SafeNet, "Reducing Cost and Time to Market with Silicon-Proven Security IP"
- Ravi Thummarukudy, General Manger of IP, GDA Technologies, "Adding High-Speed I/O Controllers to SoC Designs"



- Michael Sobelman, Director of Architecture, Rambus, "Integrating High-Speed Intra-System Interfaces"

The seminar is free to qualified individuals who register by January 20. The seminar is targeted at ASIC and SoC vendors, OEMs, press, and the financial community.

For further details and registration information, visit our web site:  
<http://www.linleygroup.com/seminars.html>

### **About The Linley Group**

The Linley Group is the leading provider of independent technology analysis for the networking-silicon industry, covering emerging areas such as access processors, network processors, communications processors, Gigabit Ethernet, switch fabrics, high-speed interconnects, security and content processors, high-speed embedded processors, and storage networking silicon. The company provides in-depth technology reports and interactive seminars as well as strategic consulting services tailored to the individual client. To get free access to The Linley Group's analysis of recent news and events in this market segment, subscribe to The Linley Wire, our e-mail newsletter. More information about The Linley Group is available at <http://www.linleygroup.com>.

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