

## PRESS RELEASE

### **Linley Tech Seminar Analyzes Multicore Processors and Next-Generation FPGAs Program Features Freescale, AMCC, Cavium, Cswitch, IBM, Xilinx, Intel, and Teja**

MOUNTAIN VIEW, CA—October 18, 2006—On November 1, The Linley Group presents a Linley Tech seminar on Programmable Devices for Network System Design. This one-day event focuses on two distinct approaches to network system design: high-speed embedded CPUs vs. advanced FPGAs. The seminar brings together industry experts and top technologists from leading vendors to deliver in-depth technical presentations covering the latest developments and products. The event is designed to assist system designers seeking a high-speed embedded processor or a high-end FPGA for networking or communications applications.

The morning session, moderated by Linley Gwennap, principal analyst with The Linley Group, will analyze trends in embedded CPU design and explore how these processors are used in network systems. The discussion will contrast multicore and single-CPU designs for both control- and data-plane processing, including recently announced products such as Cavium's Octeon Plus, Intel's embedded Core 2 processors, and IBM's PowerPC 970GX. The speakers will also address power efficiency, interconnect, and software issues.

Joseph Byrne, senior analyst with The Linley Group, will moderate the afternoon session which focuses on how to best utilize advanced FPGAs in system designs, covering brand-new products such as Xilinx's Virtex-5 LXT and Cswitch's unique configurable array. These devices include many new features to optimize network system designs. And, our ever-popular closing panel will challenge the participants to defend their views on the benefits of CPUs vs. NPU's vs. FPGAs for packet processing.

The speaker list is packed with top technical talent in this field. Our featured speakers include the following:

- Dan Bouvier, Director of Solutions Architecture at AMCC
- Raghib Hussain, Cavium's CTO and VP Software
- Srinivasan Ramani, a senior engineer at IBM
- Pranav Mehta, a senior principal engineer at Intel
- Geoffrey Waters, a senior systems engineer at Freescale
- Joe Byrne, senior analyst at The Linley Group
- Akash Deshpande, CTO of Teja Technologies
- Navneet Rao, a systems architect at Xilinx
- Godfrey D'Souza, VP Engineering at Cswitch
- Linley Gwennap, principal analyst at The Linley Group



This seminar is a great opportunity to get the information you need to utilize leading-edge technology to meet your design goals. If you don't believe us, ask someone who has attended a Linley Tech seminar: according to our surveys, more than 97% of the attendees would recommend our events to others.

The seminar, free to qualified individuals who register by October 27, is targeted at system designers, OEMs, network-equipment vendors, service providers, carriers, software developers, press, and the financial community. The seminar will be held at the Marriott Hotel in Santa Clara, CA. For further details and registration information, visit our web site: <http://www.linleygroup.com/seminars.html> The seminar is sponsored by Freescale, AMCC, Cavium, Cswitch, IBM and The Linley Group.

### **About The Linley Group**

The Linley Group, the leading provider of independent technology analysis for networking, communications, and consumer electronics semiconductors, covers emerging areas such as high-speed embedded processors, Gigabit Ethernet, wireless handset processors, access processors, security and content processors, high-speed interconnects, and more. The company provides in-depth technology reports and interactive seminars as well as strategic consulting services tailored to the individual client. To get free access to The Linley Group's analysis of recent news and events in these markets, subscribe to our e-mail newsletters. More information about The Linley Group is available at <http://www.linleygroup.com>.

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